

A Review in Advanced Digital Signal Processing Systems

Mugumya Athur¹, Akankunda Joan², Matsiko Emmanuel³, and Mohammed Dahiru Buhari⁴

Department of Electrical, Telecommunication and Computer Engineering, Kampala International University, Uganda¹

Electrical and Electronic Engineering Department, Abubakar Tafawa Balewa University, Bauchi, Nigeria²

Electrical Engineering Department, American University of the Middle East (AUM)^{3,4}

Department of Computer Engineering, Ahmadu Bello University, Zaria⁵.

athur.mugumya@studwc.kiu.ac.ug¹, joan.akankunda.21203@studwc.kiu.ac.ug²,

Emmanuel.matsiko.21407@studwc.kiu.ug³, buharim@kiu.ac.ug⁴

Corresponding Author: athur.mugumya@studwc.kiu.ac.ug¹

Paper history:

Received on 11th
January, and accepted
26th March, 2024

Keywords

*Digital signal processing
(DSP), Signal processing
methods, Machine
learning algorithms,
Denoising, Internet of
Things (IoT), 5G
networks,
Electrocardiogram
(ECG)*

Abstract

Digital Signal Processing (DSP) is a powerful technology that helps in making sense of various signals, like sounds and images, using computers. This review paper explains the meaning of DSP, shows how it works to process and enhance signals. It explores the wide range of signal processing methods, categorizing them from basic noise reduction to advanced machine learning algorithms, and how they are used today to improve the quality of audio, images, medical data, and other control systems. The paper further examines into signal processing techniques, providing a comprehensive understanding of the diverse methodologies employed in DSP applications. Additionally, it addresses not only the advancements but also the drawbacks associated with advanced DSP systems, offering insightful recommendations for overcoming challenges and optimizing performance. This review also includes categories of DSP methods, providing a structured overview of the different approaches within the field. It offers a clear and concise understanding of DSP, its practical uses, and its exciting potential in the digital age

1.0 Introduction

A signal is a representation of information or data that varies over time. It is a time-varying function that carries meaningful information. Signals can take various forms, such as audio, video, images, sensor measurements, or any data that evolves with time. These signals are often analog in nature, meaning they are continuous and vary smoothly, but to effectively manipulate and analyze them, they are converted into a digital format [1]. Digital signal processing involves the transformation of analog signals into discrete, digital representations through a process known as analog-to-digital conversion [2].

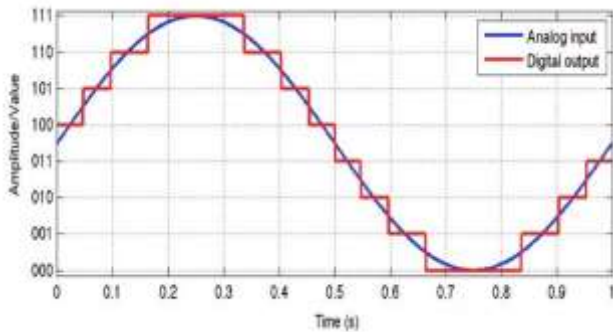


Figure 1 A continuous signal (analog) turning into a digital signal [3].

This transformation enables the application of mathematical operations, filtering, analysis, and various algorithms on the signal, facilitating tasks like noise reduction, compression, modulation, and more [4]. In the context of this paper, an Advanced Digital Signal Processing (ADSP) system refers to a sophisticated computational framework that leverages digital signal processing techniques to analyze and manipulate signals with heightened precision and efficiency, offering a wide array of applications across diverse fields [5].

In the realm of advanced Digital Signal Processing, significant contributions have been made to propel technological innovation [6]. Leveraging intricate algorithms and high performance computing, DSP has played a vital role in the development of cutting-edge applications such as real-time speech recognition, image and video compression, and advanced radar systems [7]. However, as we venture into the digital future, it is imperative to recognize and address the

challenges, trends, and drawbacks associated with DSP systems [8]. Navigating the complexities of emerging technologies requires a nuanced understanding of the computational demands, latency considerations, and energy efficiency issues inherent in advanced DSP applications. Acknowledging these factors is crucial for steering the course towards an effective and efficient digital future, ensuring that DSP continues to be at the forefront of transformative advancements across diverse domains.

2.0 Classifications of DSP Methods

Within the domain of signal processing methods and classifications, their categorization spans a diverse range of techniques uniquely suited for various signal types as follows .

- i. **Analog Signal Processing.** Analog signal processing is concerned with non-digital signals such as those present in radios, Cellular phones, and older televisions sets. This contains linear and nonlinear electronic circuits [9]. Linear circuits are comprised of integral catchers, passive filters, collector filters, and delay lines, while nonlinear circuits are comprised of components like voltage-controlled oscillators and phase-locked loops[10].
- ii. **Digital Signal Processing.** This involves the utilization of digital processing systems, often computer-based or customized signal processors, for a wide range of signal processing tasks. DSP can encompass both linear and nonlinear operations. Nonlinear DSP processes are closely associated with detecting nonlinear systems and can be applied in frequency, time, and space-time domains [11].
- iii. **Continuous Signal Processing in Time.** Time-Domain Continuous Signal Processing is designed for signals exhibiting continuous changes in amplitude, excluding occasional data points. This approach employs methods to process signal parameters related to time, frequency, and their combination. Its primary emphasis lies in the modeling of continuous linear systems with a consistent time factor. This includes the incorporation of zero system state response aggregation, refinement of system functions, and

- the application of continuous time-domain filtering to specific signals [12].
- iv. **Processing Discrete Signals in Time.** Discrete signal processing is tailored for signals sampled at specific time intervals and quantized in time, as opposed to quantity. Its implementation involves the use of electronic devices equipped with sampling and storage circuits, multiplexers, and analog delay lines. This methodology serves as the foundational framework for digital signal processing, particularly in the time of advanced GHz signal processing. The overarching concept of processing discrete signals over time establishes fundamental mathematical principles for effectively managing digital signals, irrespective of any inherent quantization errors [13].
 - v. **Nonlinear Signal Processing.** This revolves around the analysis and manipulation of signals produced by nonlinear systems, either in the time or frequency domain. Nonlinear systems exhibit intricate behaviors such as bifurcation, chaos theory, and harmonics that cannot be adequately explored using linear methods[7].

3.0 Signal Processing Techniques and Methodology

Signal processing methodology encompasses a diverse array of techniques and principles employed in the manipulation and analysis of signals, which can be analog or digital, continuous or discrete, and linear or nonlinear. It serves as the foundation for various applications across numerous fields, including telecommunications, audio and image processing, biomedical engineering, and more. The methodology involves methods for signal representation, transformation, and enhancement, utilizing mathematical tools like Fourier analysis, convolution, and various transforms.

3.1 Convolution

Convolution is a fundamental operation in signal processing, integral to tasks such as filtering, feature extraction, and system analysis. It involves the combination of two signals to produce a third, representing the mathematical integration of their

information [14]. Convolution plays an essential part in various applications, including image and speech processing, where it is used for smoothing, edge detection, and pattern recognition [15]. Recent advancements focus on optimizing convolution algorithms for real-time processing, enabling efficient handling of large datasets. The integration of convolution with deep learning architectures, such as Convolutional Neural Networks (CNNs), has significantly advanced tasks like image classification and object detection[16]. The enduring importance of convolution in extracting meaningful information from signals underscores its central role in contemporary signal processing applications.

3.2 Filtering

In digital signal processing systems, filtering techniques play a vital part in enhancing signal quality and extracting relevant information. Digital filters, such as infinite impulse response (IIR) filters and finite impulse response (FIR), are extensively employed across diverse domains [17]. Recent advancements in this realm include the implementation of adaptive filters, which dynamically adjust their parameters based on changing input signals[18]. The integration of machine learning methodologies with filtering processes has gained attention, showcasing promising results in tasks like signal denoising and feature extraction[19]. The understanding of filtering intricacies contributes significantly to the improvement of signal fidelity, making these techniques indispensable for various applications.

3.3 Transforms

Transforms are fundamental in digital signal processing, enabling the study and operation of signals across diverse domains. The Fourier Transform, a basis tool, decomposes signals into their frequency components [20]. Recent advancements include the exploration of advanced transform techniques, notably wavelet transforms, providing a more confined representation of signal features in both time and frequency domains [21]. Wavelet transforms excel in capturing transient and confined signal characteristics,

offering advantages in applications such as image compression, denoising, and feature extraction. Also the Z-transform analyses discrete-time signals and systems in the frequency domain [22]. While the Fourier Transform is limited to analyzing periodic signals, the Z-transform extends this capability to a broader range of signals, including those with transient components. Ongoing efforts in developing fast and efficient algorithms for Z-transform contribute to real-time processing capabilities, expanding its utility [20].

3.4 Sampling and Reconstruction

The process of sampling and reconstruction works in transitioning between continuous-time and discrete-time signals in digital signal processing systems. Recent research has probed into advanced sampling strategies, addressing issues like non-uniform sampling and compressed sensing [23]. The primary focus is on enhancing efficiency in signal acquisition while concurrently alleviating the burden on data storage and transmission [24]. Techniques for accurately reconstructing signals from sparse samples have been a central point of exploration, seeking a delicate balance between preserving signal integrity and minimizing resource requirements [25]. These advancements in sampling and reconstruction significantly contribute to the efficiency and robustness of digital signal processing systems, finding applications across a diverse array of fields.

3.5 Modulation and demodulation

Modulation and demodulation are fundamental processes in signal processing, crucial for efficient data transmission and communication [26]. Modulation contains altering the characteristics of a carrier signal to encode information, with techniques such as Amplitude Modulation (AM), Frequency Modulation (FM), and Phase Modulation (PM) [26]. These methods enable the transmission of information over various communication channels. Demodulation, on the other hand, is the process of extracting the original information from the modulated signal [27]. Recent advancements in modulation and demodulation techniques focus on improving data transfer rates,

signal robustness in noisy environments, and enhancing spectral efficiency [28]. Applications span a wide range, from traditional radio and television broadcasting to modern wireless communication systems, highlighting the enduring relevance and incessant evolution of modulation and demodulation in current signal processing

4.0 Trends in DSP Systems

4.1 Current Trends

Contemporary developments in DSP incorporate a growing reliance on machine learning methodologies, the design of hardware tailored for DSP functions, and the fusion of DSP with other technologies like Internet of Things (IoT) and 5G networks [29]. A prominent shift in DSP involves leveraging machine learning approaches to enhance the precision and efficiency of DSP algorithms. Applications of deep learning, such as convolutional neural networks (CNNs) and recurrent neural networks (RNNs), have been used in a variety of DSP applications such as image and speech recognition [30]. Parallely, there is a trend in crafting hardware specifically optimized for DSP functions. Field-programmable gate arrays (FPGAs) and graphics processing units (GPUs), for instance, have been harnessed to expedite DSP algorithms, delivering substantial speed enhancements compared to conventional processors. These hardware optimizations enable real-time processing of extensive datasets in a more energy-efficient manner [31]. Additionally, the convergence of DSP with other technologies, such as IoT and 5G networks, represents a significant trend. DSP plays a pivotal role in handling data from sensors in IoT devices, and efficient DSP algorithms are imperative for signal processing and data transmission in 5G networks[32]. Notably, DSP finds application in fields like autonomous vehicles, where it processes sensor data to facilitate real-time decision-making.

5.0 Applications of Digital Signal Processing Systems

Digital Signal Processing systems have found extensive applications across various fields, revolutionizing the

way signals are analyzed, manipulated, and transmitted. From audio and image processing to telecommunications, medical diagnostics, and control systems, DSP plays a key role in enhancing the efficiency and precision of signal-related tasks [21].

5.1 Autonomous Driving

The perceptiveness of the system for autonomous vehicle navigation is composed of a merger of active and passive sensors that is, cameras, radars, and lidars [33]. Lidars are active sensors that illuminate their environment by releasing lasers. Processing the received laser returns from the reflecting surfaces yields precise ranges. Despite significant progress in camera-based perception, image processing algorithms estimate distances as well. [34].

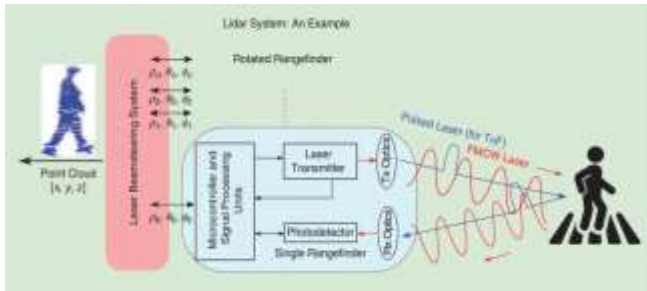


Figure 1: Use of a laser rangefinder in a lidar system [34].

In practice, lidars are used with cameras to supplement one other [34]. A camera is deficient at estimating distance, and lidar is poor at object detection. A typical lidar scans its field of vision with one or more laser beams. This is accomplished through the use of a precisely built beam steering mechanism. An amplitude-modulated laser diode emitting near-infrared light generates the laser beam. The laser beam is reflected back to the scanner by the surrounding, and the returning signal is detected by a photodetector. Fast electronics filter the signal and calculate the distance-proportional difference between transmitted and received signals. Based on this disparity, the sensor model estimates the range. Signal processing compensates for fluctuations in reflected energy caused by surface materials as well as the condition of the surroundings between the transmitter and receiver. Fast

electronics filter the signal and calculate the distance-proportional difference between transmitted and received signals. Based on this disparity, the sensor model estimates the range. Signal processing compensates for fluctuations in reflected energy caused by surface materials as well as the condition of the surroundings between the transmitter and receiver. [34]

5.2 Condition Monitoring of Faulty Machining Processes and Defected Rotary Machines

Many signals generated during Condition Monitoring of faulty machining processes and defected rotary machines are non-deterministic and non-stationary in nature, necessitating the use of an appropriate DSP technique for their analysis [35]. The Fast Fourier Transform can be used to identify irregularities in rotating machinery vibrations [7] and compressed sensing method can be used to classify the defects in bearings[10]. For motor currents; this parameter is useful in identifying induction motor faults by use of a low pass filter, FFT and averaging [36]. For flank images; image processing technique is used to keep track of the flank wear so as to replace the cutting insert in time [35].

5.3 Image Processing

Image processing puts to use digital signal processing. Among the three general categories of image processing is Low level processing, it is composed of basic digital processing techniques such as noise cancellation, image filtering, and contrast [13].



Figure 3: Image Processing Steps [13].

5.4 Brain-Computer Interface Based on Electroencephalogram (EEG)

A Brain-Computer Interface (BCI) establishes a communication link between the brain and an external

device, with particular emphasis on EEG-based BCIs, where electric potentials recorded from scalp electrodes directly measure brain activity. The BCI system comprises signal acquisition, involving electrodes, amplifiers, multiplexers, and an Analog to Digital Converter, capturing neurophysiological signals and converting them into digital values. Signal processing then interprets the raw data through data preprocessing, feature extraction, and classification, generating control commands. These commands are part of the BCI application, which includes a control interface to interpret commands and produce signals for device control. Communication interfaces and a synchronization system facilitate information transfer among BCI components, creating a closed-loop system that communicates information from the BCI application to the subject [37]. However, EEG recordings often suffer from contamination by unwanted signals, known as artifacts. These artifacts introduce noise into EEG acquisitions and can arise from both endogenous sources (such as physiological factors like eye, muscle, and cardiac activity) and exogenous sources (including non-physiological factors like impedance mismatch and power-line coupling). Various methods have been employed to address EEG artifacts, with one approach involving artifact removal through preprocessing of the EEG data [38]. This is through the use of wavelet transform or Empirical Mode decomposition with blind source separation and adaptive filtering [30].

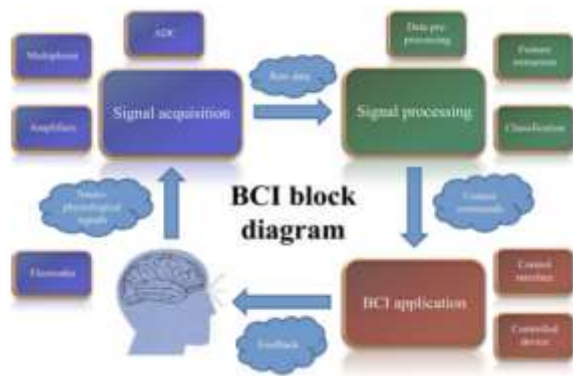


Figure 4: Brain Computer Interface Block Diagram [37].

5.5 Graph Signal Processing

The graph interpretation of DSP through signal transform techniques, specifically the z-transform and the Fourier transform with a shift, can be expanded to formulate a Generalized Signal Processing (GSP) that is linear and time-shift invariant [39]. The utilization of the z-transform offers a representation of the signal that proves valuable in the analysis of signal processing by filters [40]. Additionally, the Discrete Fourier Transform (DFT) represents the signal in the frequency domain, introducing concepts such as frequency, spectrum, and distinctions between low-band and high-pass signals [39].

5.6 Electrocardiogram (ECG)

The ECG signal provides insights into the electrical activity of the heart. To capture an ECG signal, electrodes (transducers) are strategically positioned on the human body. However, undesirable signals, known as artifacts or noise, often intertwine with the ECG signal. There are four primary types of artifacts commonly observed in ECG signals, namely baseline wander, power line interference, electromyography (EMG) noise, and artifacts arising from electrode motion [41]. To address baseline wander in ECG signals, an optimal high-pass filter is selected to eliminate this artifact. Alternatively, the Wavelet transform proves effective in mitigating baseline wander, with a frequency of approximately 0.5 Hz. Following the principles of discrete wavelet transform (DWT), the original signal undergoes decomposition using subsequent low-pass and high-pass filters, with the cut-off frequency for both filters set at half of the sampling frequency [42]. For mitigation of Powerline Interference, a second-order filter with a notch is employed, chosen for its broader bandwidth capable of attenuating powerline interference. For the reduction of electromyographic (EMG) noise, a Moving Average (MA) filter is applied, given its efficacy in handling high-frequency noise [43]. And finally, Electrode motion artifacts can be removed by the use of adaptive filters [44].

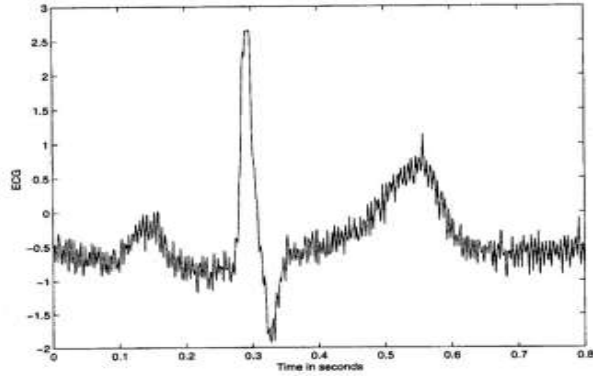


Figure 5: ECG Signal with Noise [41].

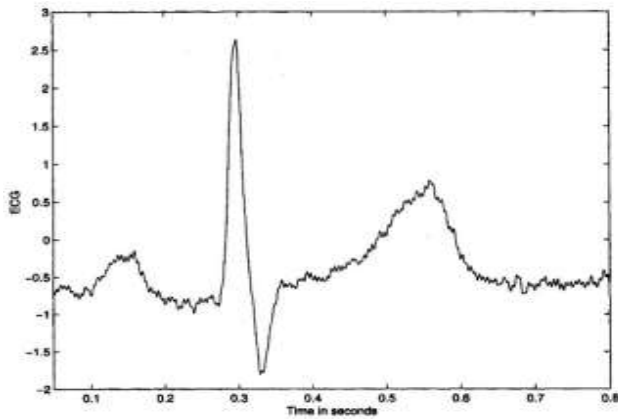


Figure 6: Same signal after filtering with 8-point MA Filter [41].

5.7 Networked Control Systems

In these systems, there exists communication between sensors, actuators, and controllers facilitated by an amalgamated band-limited digital communication network. Network Control Systems find application in diverse fields, including space exploration, environmental control, industrial automation, robotics, aircraft, automobiles, manufacturing plant monitoring, remote diagnostics and troubleshooting, and teleoperations [45][46]. In these systems, data packets are transmitted solely in the form of digital signals rather than continuous signals. This necessitates the sampling and quantization of signals from physical plants before transmission. It's important to acknowledge that signal sampling, quantization errors, network-induced delays, and packet dropouts can

impact the control performance of a Network Control System [47]. In these systems, data packets are transmitted solely in the form of digital signals rather than continuous signals. This necessitates the sampling and quantization of signals from physical plants before transmission. It's important to acknowledge that signal sampling, quantization errors, network-induced delays, and packet dropouts can impact the control performance of a Network Control System.

6.0 Challenges and Draw Backs in Digital Signal Processing

Navigating the time of digital signal processing brings forth multifaceted challenges and drawbacks that demand careful consideration. System complexity and power consumption are focal considerations in the domain of digital signal processing. The intricate nature of DSP algorithms, designed for tasks like error detection, correction, and data compression, contributes to a high level of system complexity [48]. This complexity poses challenges in terms of system design, integration, and maintenance. While DSP systems offer advantages such as high accuracy and flexibility, managing their intricacy becomes crucial to ensure efficient operation. Simultaneously, power consumption emerges as a significant concern. Despite the benefits of DSP, these systems often demand substantial power resources, particularly in real-time processing scenarios. Balancing the need for advanced processing capabilities with the imperative of energy efficiency remains a fundamental challenge [49]. Also real-time processing poses a significant challenge in various digital signal processing applications, demanding low potential to meet real-world requirements effectively. The presence of noise and interference in real-world signals introduces complexities, impacting the accuracy and reliability of digital signal processing algorithms [50]. Ensuring the precision and robustness of these algorithms is crucial, particularly in applications like medical signal processing and speech recognition, where inaccuracies could have significant consequences. Additionally, the need for large datasets in training and testing digital signal processing algorithms raises concerns about data

availability, privacy, and security [39]. The integration of machine learning introduces further challenges, including the demand for extensive labeled data and the interpretability of complex deep learning models [51]. Real-world signal complexity, characterized by non-stationary and intricate signals, presents difficulties in accurate modeling and processing. The interdisciplinary nature of digital signal processing, requiring expertise in mathematics, computer science, and engineering, makes finding individuals with the necessary skills a challenge [52]. Keeping up with evolving industry standards and best practices adds another layer of complexity, and hardware limitations in processing power and data capacity can impact the overall accuracy and speed of digital signal processing applications [53]. Addressing these challenges is essential for the continued advancement and effective implementation of digital signal processing techniques across diverse fields.

7.0 Recommendations

Based on the review of ADSP, several recommendations are expressed to guide future research endeavors. Firstly, addressing the challenges associated with real-time processing, such as low latency and high throughput requirements, should be a focal point. Exploring innovative algorithms and architectures that optimize real-time performance while maintaining accuracy is imperative. Furthermore, there is a need to enhance the robustness and accuracy of ADSP algorithms, particularly in critical applications like medical signal processing and speech recognition. Research efforts should be directed towards developing algorithms that can adapt to variations in signal quality and detect/correct errors effectively. Given the increasing reliance on machine learning in ADSP, future research should explore into methods to mitigate challenges related to data availability, privacy, and the interpretability of complex models. Additionally, tackling the complexities arising from real-world signal characteristics, which are often non-stationary and difficult, requires innovative modeling and processing techniques. The interdisciplinary nature of ADSP necessitates collaborative efforts, and initiatives to

foster interdisciplinary expertise should be encouraged. Keeping abreast of evolving industry standards and best practices is vital, urging researchers to actively engage with the dynamic landscape of ADSP. Finally, addressing hardware limitations to enhance processing power and accommodate more complex algorithms will be crucial for advancing the accuracy and efficiency of ADSP applications. These recommendations collectively aim to propel the field of ADSP towards greater effectiveness and applicability in different domains.

8.0 Conclusion

This paper underscores the paramount significance of advanced Digital Signal Processing (DSP) systems in shaping the landscape of modern signal processing methodologies. Through a careful examination of the multifaceted applications, contributions, challenges, and trends associated with DSP, it becomes evident that these systems have not only revolutionized traditional signal processing tasks but have also paved the way for unprecedented technological advancements. As the present stands on the point of a digital future, it is imperative to recognize the indispensability of DSP in domains ranging from telecommunications to medical diagnostics and beyond. However, a comprehensive understanding of the challenges and drawbacks inherent in advanced DSP is essential for ensuring the continued effectiveness and efficiency of these systems. By addressing these intricacies, researchers and practitioners can navigate the complexities of emerging technologies, steering DSP towards new frontiers and solidifying its role as a cornerstone in the relentless pursuit of innovation in various application.

References

- [1] S. F. Barrett and D. J. Pack, "Analog-to-Digital Conversion," in *Microchip AVR® Microcontroller Primer: Programming and Interfacing*, Springer, 2019, pp. 99–136.
- [2] S. Kosonocky and P. Xiao, "Analog-to-digital conversion architectures," in *Digital Signal Processing Fundamentals*, CRC Press, 2017, pp. 103–118.
- [3] M. Gudino, "Engineering Resources: Basics of Analog-to-Digital Converters," *Arrow.com [online]*, vol. 17, 2018.

- [4] T. Waho, *Introduction to Analog-to-Digital Converters*. CRC Press, 2022.
- [5] P. S. Diniz, E. A. Da Silva, and S. L. Netto, *Digital signal processing: system analysis and design*. Cambridge University Press, 2010.
- [6] L. Deng, "What is Signal Processing?," *Education*, 2023.
- [7] K. Zhong, X. Zhou, J. Huo, C. Yu, C. Lu, and A. P. T. Lau, "Digital signal processing for short-reach optical communications: A review of current technologies and future trends," *Journal of Lightwave Technology*, vol. 36, no. 2, pp. 377–400, 2018.
- [8] D. Goyal, C. Mongia, and S. Sehgal, "Applications of digital signal processing in monitoring machining processes and rotary components: a review," *IEEE Sensors Journal*, vol. 21, no. 7, pp. 8780–8804, 2021.
- [9] P. Crovetto, F. Musolino, O. Aiello, P. Toledo, and R. Rubino, "Breaking the boundaries between analogue and digital," *Electron. Lett.*, vol. 55, no. 12, pp. 672–673, 2019.
- [10] H. Ahmed and P. J. Spreadbury, *Analogue and digital electronics for engineers: an introduction*, vol. 1. Cambridge University Press, 1984.
- [11] P. A. Lynn and W. Fuerst, *Introductory digital signal processing with computer applications*. John Wiley & Sons, 1998.
- [12] B. Schell and Y. Tsividis, "Analysis of continuous-time digital signal processors," in *2007 IEEE International Symposium on Circuits and Systems*, IEEE, 2007, pp. 2232–2235.
- [13] R. Dastres and M. Soori, "Advanced image processing systems," *International Journal of Imaging and Robotics*, vol. 21, no. 1, pp. 27–44, 2021.
- [14] J. L. Rojo-Álvarez, M. Martínez-Ramón, J. Muñoz-Mari, and G. Camps-Valls, *Digital signal processing with Kernel methods*. John Wiley & Sons, 2018.
- [15] J. Jing, S. Liu, G. Wang, W. Zhang, and C. Sun, "Recent advances on image edge detection: A comprehensive review," *Neurocomputing*, 2022.
- [16] N. Sharma, V. Jain, and A. Mishra, "An analysis of convolutional neural networks for image classification," *Procedia computer science*, vol. 132, pp. 377–384, 2018.
- [17] A. Antoniou, *Digital filters: analysis, design, and signal processing applications*. McGraw-Hill Education, 2018.
- [18] A. H. Sayed and M. Rupp, "Robustness issues in adaptive filtering," in *Digital Signal Processing Fundamentals*, CRC Press, 2017, pp. 507–526.
- [19] S. Chatterjee, R. S. Thakur, R. N. Yadav, L. Gupta, and D. K. Raghuvanshi, "Review of noise removal techniques in ECG signals," *IET Signal Processing*, vol. 14, no. 9, pp. 569–590, 2020.
- [20] S. M. Alessio, "Digital signal processing and spectral analysis for scientists: concepts and applications," 2015.
- [21] T. Guo, T. Zhang, E. Lim, M. Lopez-Benitez, F. Ma, and L. Yu, "A review of wavelet analysis and its applications: Challenges and opportunities," *IEEE Access*, vol. 10, pp. 58869–58903, 2022.
- [22] S. A. Broughton and K. Bryan, *Discrete Fourier analysis and wavelets: applications to signal and image processing*. John Wiley & Sons, 2018.
- [23] P. Jungwirth and W. M. Crowe, "Continuous Time Digital Signal Processing and Signal Reconstruction," in *2023 IEEE 13th Annual Computing and Communication Workshop and Conference (CCWC)*, IEEE, 2023, pp. 1205–1211.
- [24] J. Satyanarayana, "Efficient Design of Embedded Data Acquisition Systems based on Smart Sampling," PhD Thesis, Indian Institute of Science Bangalore, 2014.
- [25] E. T. M. Beltrán et al., "Decentralized federated learning: Fundamentals, state of the art, frameworks, trends, and challenges," *IEEE Communications Surveys & Tutorials*, 2023.
- [26] H. K. Channi, "A comparative study of various digital modulation techniques," *International Journal in IT and Engineering*, vol. 4, no. 03, 2016.
- [27] K. Thyagarajan and K. Thyagarajan, "DSP in Communications," *Introduction to Digital Signal Processing Using MATLAB with Application to Digital Communications*, pp. 427–494, 2019.
- [28] M. Wen et al., "A survey on spatial modulation in emerging wireless systems: Research progresses and applications," *IEEE Journal on Selected Areas in Communications*, vol. 37, no. 9, pp. 1949–1972, 2019.
- [29] M. D. V. Pena, J. J. Rodriguez-Andina, and M. Manic, "The internet of things: The role of reconfigurable platforms," *IEEE Industrial Electronics Magazine*, vol. 11, no. 3, pp. 6–19, 2017.
- [30] H.-A. T. Nguyen et al., "EOG artifact removal using a wavelet neural network," *Neurocomputing*, vol. 97, pp. 374–389, 2012.
- [31] K. Pereira and K. Savio, "Characterization of FPGA-based high performance computers," *Blacksburg, Virginia*, 2011.
- [32] I. P. Chochliouros et al., "Energy efficiency concerns and trends in future 5G network infrastructures," *Energies*, vol. 14, no. 17, p. 5392, 2021.
- [33] J. Leonard et al., "A perception-driven autonomous urban vehicle," *Journal of Field Robotics*, vol. 25, no. 10, pp. 727–774, 2008.
- [34] Y. Li and J. Ibanez-Guzman, "Lidar for autonomous driving: The principles, challenges, and trends for automotive

lidar and perception systems,” *IEEE Signal Processing Magazine*, vol. 37, no. 4, pp. 50–61, 2020.

[35] D. Goyal, B. Pabla, S. Dhama, and others, “Non-contact sensor placement strategy for condition monitoring of rotating machine-elements,” *Engineering Science and Technology, an International Journal*, vol. 22, no. 2, pp. 489–501, 2019.

[36] M. E. H. Benbouzid, M. Vieira, and C. Theys, “Induction motors’ faults detection and localization using stator current advanced signal processing techniques,” *IEEE Transactions on power electronics*, vol. 14, no. 1, pp. 14–22, 1999.

[37] J. Minguillon, M. A. Lopez-Gordo, and F. Pelayo, “Trends in EEG-BCI for daily-life: Requirements for artifact removal,” *Biomedical Signal Processing and Control*, vol. 31, pp. 407–418, 2017.

[38] M. Fatourech, A. Bashashati, R. K. Ward, and G. E. Birch, “EMG and EOG artifacts in brain computer interface systems: A survey,” *Clinical neurophysiology*, vol. 118, no. 3, pp. 480–494, 2007.

[39] A. Ortega, P. Frossard, J. Kovačević, J. M. Moura, and P. Vanderghenst, “Graph signal processing: Overview, challenges, and applications,” *Proceedings of the IEEE*, vol. 106, no. 5, pp. 808–828, 2018.

[40] A. Sandryhaila and J. M. Moura, “Discrete signal processing on graphs,” *IEEE transactions on signal processing*, vol. 61, no. 7, pp. 1644–1656, 2013.

[41] R. Kher and others, “Signal processing techniques for removing noise from ECG signals,” *J. Biomed. Eng. Res*, vol. 3, no. 101, pp. 1–9, 2019.

[42] B. A. Sheno, *Introduction to digital signal processing and filter design*, vol. 169. John Wiley & Sons, 2005.

[43] R. M. Rangayyan, *Biomedical signal analysis*. John Wiley & Sons, 2015.

[44] M. Milanese et al., “Multichannel techniques for motion artifacts removal from electrocardiographic signals,” in *2006 International Conference of the IEEE Engineering in Medicine and Biology Society*, IEEE, 2006, pp. 3391–3394.

[45] H. Sandberg, S. Amin, and K. H. Johansson, “Cyberphysical security in networked control systems: An introduction to the issue,” *IEEE Control Systems Magazine*, vol. 35, no. 1, pp. 20–23, 2015.

[46] L. Ding, Q.-L. Han, E. Sindi, and others, “Distributed cooperative optimal control of DC microgrids with communication delays,” *IEEE Transactions on Industrial Informatics*, vol. 14, no. 9, pp. 3924–3935, 2018.

[47] M. S. Mahmoud and M. M. Hamdan, “Fundamental issues in networked control systems,” *IEEE/CAA Journal of Automatica Sinica*, vol. 5, no. 5, pp. 902–922, 2018.

[48] R. Dastres and M. Soori, “A Review in Advanced Digital Signal Processing Systems,” *International Journal of Electrical and Computer Engineering*, 2021.

[49] R. Dastres and M. Soori, “A Review in Advanced Digital Signal Processing Systems,” *International Journal of Electrical and Computer Engineering*, 2021.

[50] Y. Yang, J. P. Lie, and A. Quintero, “Low complexity implementation of carrier and symbol timing synchronization for a fully digital downhole telemetry system,” in *2018 IEEE International Conference on Acoustics, Speech and Signal Processing (ICASSP)*, IEEE, 2018, pp. 1150–1153.

[51] X. Dong, D. Thanou, L. Toni, M. Bronstein, and P. Frossard, “Graph signal processing for machine learning: A review and new perspectives,” *IEEE Signal processing magazine*, vol. 37, no. 6, pp. 117–127, 2020.

[52] K. D. Rao and M. N. Swamy, *Digital signal processing: Theory and practice*. Springer, 2018.

[53] S.-K. Ong, M. M. L. Chang, and A. Y. Nee, “Product disassembly sequence planning: state-of-the-art, challenges, opportunities and future directions,” *International Journal of Production Research*, vol. 59, no. 11, pp. 3493–3508, 2021.